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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/681,695	05/22/2001	Terence J. Murphy	TI-31565	5300

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EXAMINER
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GONZALEZ, JULIO C

ART UNIT	PAPER NUMBER
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2834

DATE MAILED: 04/24/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

09/681,695

Applicant(s)

MURPHY ET AL.

Examiner

Julio C. Gonzalez

Art Unit

2834

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 01 February 2002.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-21 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-21 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 22 May 2001 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 2.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

**DETAILED ACTION**

***Election/Restrictions***

1. Applicant's election with traverse of claims 1-14 in Paper No. 4 (Response to restriction) is acknowledged. The traversal is on the ground(s) that inventions I and III are related as product and process of use. This is not found persuasive because a current mirror is not required to only have two outputs as required by claim 22.

Moreover, the current output of the current mirrors having a ratio of  $1X:nX$  is not required by invention I.

About Groups I (claims 1-14) and Group II (claims 15-21), the examiner agrees with the applicant as for those two groups may be classified in one group; therefore, this office action will take into consideration claims 1-21.

The requirement is still deemed proper and is therefore made FINAL.

***Drawings***

2. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they include the following reference sign(s) not mentioned in the description: 100, 102 in figure 3A. A proposed drawing correction, corrected drawings, or amendment to the specification to add the reference sign(s) in the description, are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

3. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(4) because reference character "16" has been used to designate both spindle drive and drive circuit; character "18" has been used to designate both disk and data transducer; reference character "72" has been used to designate both analog ground and voltage source. A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

4. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the voltage supply other than a voltage supply for the piezo element as disclosed in claim 3 and the feedback path disclosed in claim 6 must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

***Claim Rejections - 35 USC § 112***

5. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

6. Claims 1-21 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

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In claim 1, what is meant by the voltage supply been "referenced" to a substrate potential? Is the claim implying that the voltage of the voltage supply is the same as the substrate potential?

In claim 3, what is meant by "a voltage supply other than a voltage supply" for the piezo element? Is the voltage supply used for other components? Or are there two voltage supplies, one being the piezo element and the other voltage supply another voltage supply for the other components "other than a voltage supply" for the piezo element?

In claim 6, what is meant by "selectively disable" the first circuit? Is the first circuit disabled when the second circuit is not disabled? Or are the first and second circuit disabled at the same time?

Also, what is meant by the second switch providing a feedback path from the second circuit to an input to the second circuit? Where is the feedback starting?

Is the integrated circuit operating in voltage mode only? What determines the integrated circuit to be operating in voltage mode?

In claim 15 which part of the circuit is functioning as the first current mirror? The AB amplifier (figure 2)? From the figures, it seems like if only one current mirror has been point out.

In claim 20, which are the switches disabling the first and second current mirrors?

In order to advance prosecution in the merits, the Prior Art will be applied as best understood by the examiner.

***Claim Rejections - 35 USC § 103***

7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

8. Claims 1-4, 7-10, 13-17, 20 and 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kondou in view of Stuebing et al.

Kondou discloses an integrated circuit having a first current source 106 and second current source 107 wherein a first switch  $SW_L$  and second switch  $SW_T$  can disable the current mirror circuits (see figure 2). Also, an external capacitor C is disclosed, which is part of circuit 108 and a piezo element 113 is disclosed (see figure 4). Moreover, another voltage source  $V_{cc}$  (+30V) is disclosed other than the piezo element and the first circuit has analog grounds (see figure 4).

However, Kondou does not disclose cascading several current sources.

On the other hand, Stuebing et al teaches for the purpose of obtaining a wide range of accurate time signals that it is well known in the art to place current sources in a cascade mode (see figure 6).

It would have been obvious to one having ordinary skill in the art at the time the invention was made to design an integrated circuit as disclosed by Kondou and to modify the invention by placing the current sources in cascade mode for the purpose of obtaining a wide range of accurate time signals as disclosed by Stuebing et al.

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9. Claims 5, 6, 11, 12, 18 and 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kondou in view of Stuebing et al as applied to claims 1, 8 and 15 above, and further in view of ordinary skill in the art.

The combined integrated circuit discloses all of the limitations above. However, the combined integrated circuit does not disclose the mirror ratios of 10:1 and 6.125:1.

It would have been obvious to one having ordinary skill in the art at the time the invention was made to discover those optimum ratios, since it has been held that discovering the optimum value of result effective variable involves only routine skill in the art. *In re Boesch*, 617 F. 2d 272, 205 USPQ 215 (CCPA 1980).

***Conclusion***


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Julio C. Gonzalez whose telephone number is (703) 305-1563. The examiner can normally be reached on M-F (8AM-5PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nestor Ramirez can be reached on (703) 308-1371. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 305-1341 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Jcg

April 18, 2002

  
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